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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/050,347
Filing Date January 15, 2002
Inventor Gurtej S. Sandhu et al.
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Schillinger, L.
Attorney's Docket No. MI22-1897
Title: Methods of Forming Transistors

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References –See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated:

July 19, 2002

By:

Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711

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